



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No. : 10/815,856  
Applicant : Ralf Schneider et al.  
Filed : April 2, 2004  
TC/A.U. : 2818  
Examiner : Pham, Trong Q.  
Confirmation No. : 9894  
Docket No. : 3000.0046C (0928.0046C)  
Customer No. : 054500  
Title : Integrated Memory Having a Voltage Generator  
Circuit for Generating a Voltage Supply for a  
Read/Write Amplifier

**Mail Stop Issue Fee**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**ISSUE FEE TRANSMITTAL**

Transmitted herewith is an Issue Fee Transmittal (Form PTOL 85b) for the above-identified application.

Also enclosed is:

- ☒ Amendment Under 37 C.F.R. 1.312 (4 pages) with 2 attached sheets of replacement drawings.

Fees:

- ☒ Issue Fee of \$1400.00  
☒ Other Fees: \$300.00 for Publication Fee.

Total fee: \$1700.00

Payment of Fees:

- ☒ Check No. 9207 in the amount of \$1700.00 for the total fee is attached.
- ☐ Please charge \$\_\_\_\_\_ to Deposit Account No. 05-0460 for the total fee. This paper is being submitted in duplicate.

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☒ The Commissioner is hereby authorized to charge any additional fees that may be required, and to credit any overpayment, to Deposit Account No. 05-0460.

Dated: 11/23/05

**EDELL, SHAPIRO & FINNAN, LLC**  
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Respectfully submitted by  
**EDELL, SHAPIRO & FINNAN, LLC**

By:

  
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Patrick J. Finnan  
Reg. No. 39,189